



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

**PATENT APPLICATION**

Applicant: **Kurita, et al.**

Case: **3790/USA/AKT**

Serial No.: **09/464,362**

Filed: **December 15, 1999**

Examiner: **MacArthur, Sylvia**

Group Art Unit: **1763**

Confirmation No.: **7438**

Title: **DUAL SUBSTRATE LOADLOCK PROCESS EQUIPMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R:

**SUBMISSION OF FORMAL DRAWINGS**

The Applicants submit herewith 13 sheets of formal drawings (FIGS. 1 through 17) in connection with the above-captioned application.

Respectfully submitted,

Mar 17, 2004

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CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)

I hereby certify that this correspondence is being deposited on March 17, 2004 with the United States Postal Service as first-class mail, with sufficient postage, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Allyson M. DeVesty  
Signature

3-17-04  
Date of signature